

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	969	703/22.ccls.	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/01/08 18:58

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	ISS and (RTL near verif\$7) and @ad<"20031001"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/01/09 14:13
L2	2	(ISS near verif\$7) and @ad<"20031001"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/01/09 14:14
S7	3	(PLI or (programming adj language adj interface)) and (ISS or (instruction adj set adj simulat\$4)) and RTL and @ad<"20031001"	USPAT	OR	OFF	2008/01/09 11:45
S8	3	(PLI or (programming adj language adj interface)) and (ISS or (instruction adj set adj simulat\$4)) and RTL and @ad<"20031001"	US-PGPUB; USPAT	OR	OFF	2008/01/09 13:49
S9	3	(PLI or (programming adj language adj interface)) and (ISS or (instruction adj set adj simulat\$4)) and RTL and @ad<"20031001"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/01/09 11:45
S10	16	(PLI or (programming adj language adj interface)) and (ISS or (instruction adj set adj simulat\$4)) and @ad<"20031001"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/01/09 11:58
S11	4389	(ISS or (instruction adj set adj simulat\$4)) and @ad<"20031001"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/01/09 11:58
S12	64	(ISS or (instruction adj set adj simulat\$4)) and RTL and @ad<"20031001"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/01/09 11:59
S13	132	(ISS or (instruction adj set adj simulat\$4)) and (RTL or Verilog or VHDL) and @ad<"20031001"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/01/09 14:12
S14	2	"6295623".pn.	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/01/09 12:06
S15	2	"6009256".pn.	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/01/09 12:06
S16	0	("2004/0117747").URPN.	USPAT	OR	OFF	2008/01/09 12:07

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	ISS and (RTL near verif\$7) and @ad<"20031001"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/01/09 14:13
L2	2	(ISS near verif\$7) and @ad<"20031001"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/01/09 15:07
L3	2	"6,077,304".pn.	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/01/09 15:33
L4	2	"5845064".pn.	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/01/09 15:33
L5	6	("5845064").URPN.	USPAT	OR	OFF	2008/01/09 15:43
L6	9	("5438673" "5475852" "5845064" "5859962" "5859999" "5860017" "6285914" "6332201" "6643800").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/01/09 15:51
L7	11	("5438673").URPN.	USPAT	OR	OFF	2008/01/09 15:59
L8	0	("5822226").URPN.	USPAT	OR	OFF	2008/01/09 16:04
L9	4	("5202889" "5438673" "5488573" "5568407").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/01/09 16:04
L10	0	("5822226").URPN.	USPAT	OR	OFF	2008/01/09 16:07
L11	4	("5202889" "5438673" "5488573" "5568407").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/01/09 16:07
L12	3	("5991529").URPN.	USPAT	OR	OFF	2008/01/09 16:08
L13	10	("5357626" "5438673" "5452437" "5475852" "5845064" "5859962" "5859999" "5860017" "5892897" "5923567").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/01/09 16:08
L14	9	("5438673" "5475852" "5845064" "5859962" "5859999" "5860017" "6285914" "6332201" "6643800").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/01/09 16:14


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PP Jain, CAEP Inc, TX Austin - Circuits and Systems, 1999. ISCAS'99. Proceedings of the


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L Séméria, A Ghosh - Proceedings of the 2000 conference on Asia South Pacific ..., 2000 - portal.acm.org

... First, the **verification** of the functional correctness of an applica- tion written in assembly code can be performed using **ISS**. For ...

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Possible correlation between blood glucose concentration and the reduced scattering coefficient of ... - all 7 versions »

JS Maier, SA Walker, S Fantini, MA Franceschini, E ... - Opt. Lett, 1994 - OSA

... 22 **Iss**. 3 Experimental **verification** of a theory for the time-resolved fluorescence spectroscopy of thick tissues Applied Optics Vol. 36 **Iss**. ...

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Formal Methods for the International Space Station **ISS** - all 3 versions »

J Peleska, B Buth - Correct System Design: Recent Insights and Advances, 1999 - books.google.com

... and expe- riences obtained from a **verification**, simulation and ... designed and developed by DaimlerChrysler Aerospace for the **International Space Station ISS**. ...

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Model validation and **verification** of large and complex space structures

DC Zimmerman - Inverse Problems in Science and Engineering, 2000 - informaworld.com

... **MODEL VERIFICATION OF STRUCTURES 99** ... The on-orbit construction of the **International**

Space Station (ISS) has just begun, with a total of 91 planned incremental ...

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M Puig-Medina, G Ezer, P Konas - Proceedings of the Design Automation Conference (DAC2000), 2000 - doi.ieeecomputersociety.org

... verifying the **ISS** model through the execution of extensive test suites and application code before using it as the golden model in the **verification** of a ...

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Model for photon migration in turbid biological media - all 11 versions »

RF Bonner, R Nossal, S Havlin, GH Weiss - J. Opt. Soc. Am. A, 1987 - OSA

... 16 **Iss**. 12, (1999) Experimental **verification** of a theory for the time-resolved fluorescence spectroscopy of thick tissues Applied Optics Vol. 36 **Iss**. ...

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 (CODES/CASHE'98) ..., 1998 - ieexplore.ieee.org

... adapt the differences among ISS's. Based ... J. Rowson, "Hardware/Software Co-
 simulation",

Proceedings of ... Reduction in Timed Cosimulation" Proceedings of ...

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P Gerin, S Yoo, G Nicolescu, AA Jerraya - Proceedings of the 2001 conference on Asia
 South Pacific ..., 2001 - portal.acm.org

... Tx Rx User I/F model CAP Base station model (c) Cosimulation with four ISS's and
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... Fig. 7 SystemC/ISS co-simulation 5.3 SystemC+VHDL+ISS ... BANK 3 BANK 2
 BANK 1 BANK 0

4 Banks of ROM 4 Banks of RAM Fig. 8 VHDL / ISS co-simulation test case ...

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[PDF] Fast Prototyping of an IS-95 CDMA Cellular Phone: a Case Study - all 3 versions »

S Yoo, J Lee, J Jung, K Rha, Y Cho, K Choi - Proc. the 6th Conference of Asia Pacific
 Chip Design ..., 1999 - poppy.snu.ac.kr

... interface stars and the ARM7 ISS can consume ... Overhead Reduction in Timed
 Cosimulation",

Proc. ... [4] JA Rowson, "Hardware/Software Co-Simulation", Proc. ...

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Timed HW-SW cosimulation using native execution of OS and application SW - all 6 versions »


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 ... Mixed-level **cosimulation** can also be performed. For in- stance, SW simulation
 models

 can be selected from **RTL**, eg **ISS**'s and HW simulation models can be ...

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 ... This **co-simulation** scenario focuses only on the firmware running on the **ISS**. ... bus
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 M Bacivarov, S Yoo, AA Jerraya - High-Level Design Validation and Test Workshop,
 2002. ..., 2002 - [ieeexplore.ieee.org](#)

 Page 1 0-7803-7655-2/02/Si 7.0Gg,2002 IEEE 51 Timed 11W-SW Co simulation Using
 Native

 Execution of OS and ... Figure 1 (b) shows **ISS**-based **cosimulation** method ...

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L Benini, D Bertozzi, D Bruni, N Drago, F Fummi, M ... - Computer Design: VLSI in Computers and Processors, 2002. ..., 2002 - ieeexplore.ieee.org

... will refer to this solution as "**RTL**" simulation, to ... instruction-set simulators within the **co-simulation** environment. ... The **ISS** and the C/C++ co-simulator run ...

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M Bacivarov, S Yoo, AA Jerraya - High-Level Design Validation and Test Workshop, 2002. ..., 2002 - ieeexplore.ieee.org

... 2/02/Si 7.0Gg, 2002 IEEE 51 Timed 11W-SW **Co simulation** Using Native ... in Figure 1 (a).

The BFM works as a **cosimulation** interface between the **ISS** and the ...

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Verification of transaction-level SystemC models using RTL testbenches

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 ... a coherent methodology for architecture evaluation and **co-verification** of hardware ... the software directly on the **RTL**, even co-simulated with an **ISS**, due to ...

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Towards a new standard for system-level design - all 11 versions »

 SY Liao - Proceedings of the Eighth International Workshop on Hardware ..., 2000 - [doi.ieeecomputersociety.org](#)

 ... until a synthesizable behavioral or **RTL** form is ... is a key component in any **co-verification** solution; it is ... process, an instruction-set simulator (**ISS**) needs ...

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 ... Linked **RTL** Remote ... the interface, obtained by transparently em- bedding the **ISS** within the ... A. Ghosh, "Methodology for Hardware/Software **Co-verification** in C ...

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 C Chevallaz, N Mareau, F Ghenassia- ... - Proc. of Design, Automation and Test in Europe Conference (..., 2002 - [bitchip.co.kr](#)

 ... The **co-verification** platform enabled the early integration of software on the ... is now convinced of the gain of the '**RTL** Platform' with **ISS** and will ...

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 F Schirrmeister, M Meindl, S Krolikoski - Proceedings of the 9th IEEE/DATC Electronic Design Processes ..., 2002 - [googlealert.com](#)

 ... made available through OMI for co-simulation and **co-verification**. ... design kit will be the actual **RTL** model plus ... For actual verification **ISS** models have to be ...

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Methodology for Hardware/Software **Co-verification** in C/C+ - all 9 versions »

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